

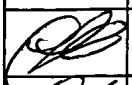


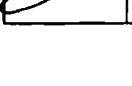
# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application No.	10/766,159
Filing Date	January 27, 2004
First Named Inventor	Erik Brockmeyer
Art Unit	2621
Examiner	Jose L. Couso
Attorney Docket No.	IMEC92.001DV1

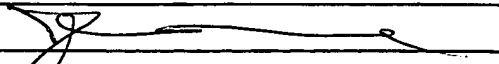
(Multiple sheets used when necessary)

SHEET 1 OF 1

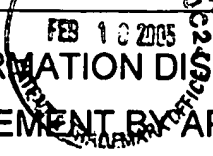
## NON PATENT LITERATURE DOCUMENTS



Examiner Initials	Date No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>1</sup>
	1	Committee Draft, Information technology-coding of audio-visual objects: Visual, N1902 "International organization for standardization, ISO/IEC/JTC1/SC29WG11," Nov 1997.	
	2	Kulkarni, C. et al., "Cache Optimization for Multimedia Compilation on Embedded Processors for Low Power," accepted for Intl. Parallel Proc. Symp. (IPPS), Orlando FL, April 1997.	
	3	Leonardo Chiariglione - Convenor, "MPEG Workplan," <a href="http://drogo.cselt.stet.it/mpeg/workplan.htm">http://drogo.cselt.stet.it/mpeg/workplan.htm</a>	
	4	VLSI Technology, Inc., "Datasheet book: 0.6-micron 5-V core mixed 5V / 9V I/O cell-based libraries," VLSI Technology, Inc., San Jose, US, Aug. 1994.	

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010505


Examiner Signature 	Date Considered <u>6/6/05</u>
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	

T<sup>1</sup> - Place a check mark in this area when an English language Translation is attached.

 <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Application No.	10/766,159
	Filing Date	January 27, 2004
	First Named Inventor	Brockmeyer, et al.
	Art Unit	2621
(Multiple sheets used when necessary)	Examiner	Jose L. Couso
SHEET 1 OF 1	Attorney Docket No.	IMEC92.001DV1

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	1	De Greef, E. et al., "Memory Size Reduction through Storage Order Optimization for Embedded Parallel Multimedia Applications," Intl. Parallel Proc. Symp. (IPPS) in Proc. Workshop on "Parallel Processing and Multimedia," Geneva, Switzerland, pp.84-98, April 1997.	
	2	Patterson, D.A. et al., "Computer Organization and Design: the Hardware/Software Interface," Morgan Kaufmann Publishers, NY, 1994, table of contents.	

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020805

Examiner Signature 	Date Considered 6/6/05
<p>*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	

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